

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1.-38. (Canceled)

39. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

40. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer;

a layer comprising a resinous material covering the thin film transistor; and  
a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

41. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

42. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

43. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

44. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

45. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

46. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

~~a thin film transistor formed over one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

47. (Currently Amended) A display device comprising:

~~a pair of resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

~~a thin film transistor formed over one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

48. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

49. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

50. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a silicon oxide film covering the thin film transistor ~~formed~~, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

51. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

52. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

53. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and



a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

54. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

55. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region

comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

56. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

57. (Currently Amended) A display device comprising:

a pair of resinous substrates facing each other, ~~wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of resinous substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of resinous substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of resinous substrates~~ transistor.

58. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, ~~wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate~~ other and having an uneven surface;

a resinous layer formed on at least one of the pair of flexible substrates, wherein the resinous layer planarizes the uneven surface;

a thin film transistor formed over ~~one of the pair of flexible substrates~~ the resinous layer, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor, ~~wherein a resinous layer is provided on a surface of one of the pair of flexible substrates~~ transistor.

59. (Previously Presented) A display device according to any one of claims 55-58, wherein the laser light comprises at least one selected from the group consisting of KrF excimer laser light and XeCl laser light.

60. (Previously Presented) A display device according to any one of claims 39-58, wherein the resinous layer comprises an acrylic resin.

61. (Previously Presented) A display device according to any one of claims 39-58, wherein the resinous layer comprises at least one selected from the group consisting of methyl esters of acrylic acid, ethyl esters of acrylic acid, butyl esters of acrylic acid, and 2-ethylhexyl esters of acrylic acid.

62.-65. (Canceled)

66. (Previously Presented) A display device according to any one of claims 35-58, wherein the thin film transistor comprises a coplanar thin-film transistor.

67. (Previously Presented) A display device according to any one of claims 35-58, wherein the thin film transistor comprises an inverted-staggered thin-film transistor.

68. (New) A display device according to any one of claims 35, 37, 39, 41, 43, 45, 47, 49, 51, 53, 55 and 57, wherein the pair of resinous substrates comprise at least one selected from the group consisting of PET (polyethylene terephthalate), PEN (polyethylene naphthalate), PES (polyethylene sulfite), and polyimide.

69. (New) A display device according to any one of claims 36, 38, 40, 42, 44, 46, 48, 50, 52, 54, 56 and 58, wherein the pair of flexible substrates comprise at least

one selected from the group consisting of PET (polyethylene terephthalate), PEN (polyethylene naphthalate), PES (polyethylene sulfite), and polyimide.